



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Onchuen (Daryn) Lau, et al

Assignee:

IDT (was ZettaCom)

Title:

Variably Delayable Transmission of Packets Between Independently

Clocked Source, Intermediate, and Destination Circuits While Maintaining Orderly and Timely Processing in One or Both of the

Intermediate and Destination Circuits

Application No.:

09/997,507

Filing Date:

11/26/2001

Examiner:

Ali, Syed J.

Group Art Unit:

2195

Docket No.:

Zetta-1004us0 [idt-1962]

Confirm No.:

7256

San Jose, California Tuesday, February 21, 2006

COMMISSIONER FOR PATENTS P.O. BOX 1450 ALEXANDRIA, VA 22313-1450

## **RESPONSE TO RESTRICTION WITH TRAVERSE**

Dear Sir:

In response to the Office Action having a mailing date of January 20, 2006, Applicant hereby provisionally elects **Group I** (Claims 1 and 5-21) with traverse.

## Remarks

It is noted that Monday, Feb. 20, 2006 was a Federal Holiday and thus response is timely even though filed Feb. 21.

LAW OFFICES OF MacPherson, Kwok, Chen & Heid LLP After re-accessing the situation together with the examiner, Applicant will have to agree that Claim 2 is distinct since it is directed to the "rates creep" whereas Claim 1 (a method claim) is directed to the problem of synchronizing operations according to the time frame of a job processor even though requests come from an independently clocked job requestor.

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